

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
6	BRS	L6	9	(Amplif\$9 with RF or amplif\$10 with radio adj2 frequenc\$3) same (multi\$3 near2 gain\$1 or plural\$3 near2 gain\$1 or multi\$3 near2 amplif\$10 or plural\$3 near3 amplif\$10) same (PLL\$1 or DPLL\$1 or phase adj1 lock\$3 adj2 loop\$1)	USPA T; US-P GPUB ; EPO; JPO	2004/11/05 13:41	
7	BRS	L7	693	(multi\$3 near2 gain\$1 or plural\$3 near2 gain\$1 or multi\$3 near2 amplif\$10 or plural\$3 near3 amplif\$10 or Gain\$1 near3 stage\$1 or plural\$3 near2 AGC or multi\$3 near2 AGC or ser\$5 near2 gain\$1 or ser\$5 near3 amplif\$10) same (PLL\$1 or DPLL\$1 or phase adj1 lock\$3 adj2 loop\$1)	USPA T; US-P GPUB ; EPO; JPO	2004/11/05 13:40	
8	BRS	L8	248	(multi\$3 near2 gain\$1 or plural\$3 near2 gain\$1 or multi\$3 near2 amplif\$10 or plural\$3 near3 amplif\$10 or Gain\$1 near3 stage\$1 or plural\$3 near2 AGC or multi\$3 near2 AGC or ser\$5 near2 gain\$1 or ser\$5 near3 amplif\$10) with (PLL\$1 or DPLL\$1 or phase adj1 lock\$3 adj2 loop\$1)	USPA T; US-P GPUB ; EPO; JPO	2004/11/05 13:56	
9	BRS	L9	34	(Amplif\$9 with RF or amplif\$10 with radio adj2 frequenc\$3) and 8	USPA T; US-P GPUB ; EPO; JPO	2004/11/05 13:59	

	Error Definition	Er ro rs
6		0
7		0
8		0
9		0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
10	BRS	L10	6026	(multi\$3 near2 gain\$1 or plural\$3 near2 gain\$1 or multi\$3 near2 amplif\$10 or plural\$3 near3 amplif\$10 or Gain\$1 near3 stage\$1 or plural\$3 near2 AGC or multi\$3 near2 AGC or ser\$5 near2 gain\$1 or series near3 amplif\$10 or serial\$4 near3 amplif\$10 or first adj2 amplif\$10 with second amplif\$10) with (PLL\$1 or DPLL\$1 or phase adj1 lock\$3 adj2 loop\$1)	USPA T; US-P GPUB ; EPO; JPO	2004/11/0 5 13:58	
11	BRS	L11	242	(multi\$3 near2 gain\$1 or plural\$3 near2 gain\$1 or multi\$3 near2 amplif\$10 or plural\$3 near3 amplif\$10 or Gain\$1 near3 stage\$1 or plural\$3 near2 AGC or multi\$3 near2 AGC or ser\$5 near2 gain\$1 or series near3 amplif\$10 or serial\$4 near3 amplif\$10 or first adj2 amplif\$10 with second adj2 amplif\$10) with (PLL\$1 or DPLL\$1 or phase adj1 lock\$3 adj2 loop\$1)	USPA T; US-P GPUB ; EPO; JPO	2004/11/0 5 14:01	

	Error Definition	Er ro rs
10		0
11		0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
12	BRS	L12	7985	(multi\$3 near2 gain\$1 or plural\$3 near2 gain\$1 or multi\$3 near2 amplif\$10 or plural\$3 near3 amplif\$10 or Gain\$1 near3 stage\$1 or plural\$3 near2 AGC or multi\$3 near2 AGC or ser\$5 near2 gain\$1 or series near3 amplif\$10 or serial\$4 near3 amplif\$10 or first adj2 amplif\$10 with second adj2 amplif\$10) with (FB or feedback or loopfilter\$1 or loop\$1)	USPA T; US-P GPUB ; EPO; JPO	2004/11/05 14:00	
13	BRS	L13	41	(Amplif\$9 with RF or amplif\$10 with radio adj2 frequenc\$3) and 11	USPA T; US-P GPUB ; EPO; JPO	2004/11/05 14:00	
14	BRS	L14	0	5874862.URPN.	USPA T	2004/11/05 14:16	
15	BRS	L15	0	5874862.URPN.	USPA T	2004/11/05 14:16	
16	BRS	L16	11	("4208635" "4243949" "4278947" "4506232" "4514706" "4752749" "4788512" "4952887" "5121085" "5369376" "5534823").PN.	USPA T	2004/11/05 14:16	
17	BRS	L17	3	(multi\$3 near2 VGA or plural\$3 near3 VGA or multi\$3 near3 LNA or plural\$3 near3 LNA or VGA near3 stage\$1 or LNA near3 stage\$1) with (PLL\$1 or DPLL\$1 or phase adj1 lock\$3 adj2 loop\$1)	USPA T; US-P GPUB ; EPO; JPO	2004/11/05 14:32	
18	BRS	L18	39	6049702.URPN.	USPA T	2004/11/05 14:27	

	Error Definition	Er ro rs
12		0
13		0
14		0
15		0
16		0
17		0
18		0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
19	BRS	L19	17	("3662294" "4931799" "5015973" "5486797" "5511238" "5515047" "5578976" "5604470" "5640049" "5673054" "5679971" "5691676" "5731607" "5774792" "5834975" "5872489" "5878331").PN.	USPA T	2004/11/0 5 14:30	
20	BRS	L20	4	(multi\$3 near2 VGA or plural\$3 near3 VGA or multi\$3 near3 LNA or plural\$3 near3 LNA or VGA near3 stage\$1 or LNA near3 stage\$1 or series near2 VGA or serial\$4 near3 VGA or series near3 LNA or serial\$4 near3 LNA) with (PLL\$1 or DPLL\$1 or phase adj1 lock\$3 adj2 loop\$1 or loop\$1 near3 offset\$3 or loop\$1 near3 cancel\$8)	USPA T; US-P GPUB ; EPO; JPO	2004/11/0 5 14:34	
21	BRS	L21	18	(multi\$3 near2 VGA or plural\$3 near3 VGA or multi\$3 near3 LNA or plural\$3 near3 LNA or VGA near3 stage\$1 or LNA near3 stage\$1 or series near2 VGA or serial\$4 near3 VGA or series near3 LNA or serial\$4 near3 LNA) same (PLL\$1 or DPLL\$1 or phase adj1 lock\$3 adj2 loop\$1 or loop\$1 near3 offset\$3 or loop\$1 near3 cancel\$8)	USPA T; US-P GPUB ; EPO; JPO	2004/11/0 5 14:37	
22	BRS	L22	14	21 not 20	USPA T; US-P GPUB ; EPO; JPO	2004/11/0 5 14:35	

	Error Definition	Er ro rs
19		0
20		0
21		0
22		0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
23	BRS	L23	134	(first adj2 amplif\$12 with second adj2 amplif\$12) same (PLL\$1 or DPLL\$1 or phase adj1 lock\$3 adj2 loop\$1 or loop\$1 near3 offset\$3 or loop\$1 near3 cancel\$8)	USPA T; US-P GPUB ; EPO; JPO	2004/11/0 5 14:38	

	Error Definition	Er ro rs
23		0